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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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APPL NUM 10041796	FILING DATE 01/07/2002	CLASS 360	SUBCLASS 46	GAU 2651	EXAMINER <i>Figueras</i>
**APPLICANTS: Cheung Wayne; Jove Stephen;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		IBM1P009/SJ0920010140US1	
Verified and Acknowledged Examiners's initials					
TITLE : CMOS low-noise MR read head pre-amplifier circuit					

U.S. DEPT. OF COMM /PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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